

High Output Power Density and Low Leakage Current of InGaN/GaN Nanorod Light Emitting Diode with Mechanical Polishing Process

Liang-Yi Chen, Ying-Yuan Huang, Chun-Siang Chang and JianJang Huang*

Graduate Institute of Photonics and Optoelectronics, National Taiwan University,
1, Roosevelt Road, Section 4, Taipei, 106, Taiwan
Phone:+886-2-3366-3534, Fax:+886-2-2367-7467, E-Mail: jjhuang@cc.ee.ntu.edu.tw

Keywords: Nanorod ,GaN, LED, CMP

Abstract

We improve our fabrication of nanorod LED with mechanical polishing method. Our nanorod LED can achieve 6807mW/cm² of output power density at 32A/cm² of injection current density. The reversed-bias leakage current has also been reduced to nA level with the mechanical polishing process. Moreover, the investigation of the droop effect for such a nanorod LED array reveals that junction heating is responsible for the sharp decrease at the low current.

INTRODUCTION

The crisis of energy is an inevitable problem in our future not faraway. Solid-State lighting device can provide a new way of human illumination which consume much less energy compared to conventional lamps. It is imperative that LED becomes the most important role in this revolution. The field of high power SSLD has been ruled by GaN based LED over a decade. Recently, many studies of GaN based nanorod structured LED has been reported. Not only the higher surface to volume ratio of nanorod structure could increase the light extraction efficiency of LED, but also the low-dimensional characteristic of nanorod intrigue us to study the physical confinement effect of carriers, photons and phonons.

The crucial issue of nanorod LED fabrication is to prevent the P-N short circuit while making the P-type contact. There are several solutions and achievements, including our previous work which realized nanorod LED by inserting space layer [1], photo-enhanced wet (PEC) chemical oxidation of GaN to reduce reversed bias current to the level of μA [2] and oblique indium tin oxide(ITO) deposition [3] can achieve output power density of 3700mW/cm² at the current density of 22.22A/cm².

In this study, we present a new method to fabricate GaN nanorod structured LED easily which leads to lower reversed bias current, uniform light emission and high output power density.

FABRICATION

The GaN based LED sample is grown by metal organic chemical vapor deposition (MOCVD) on c-plane sapphire

substrate. The epi-structure is composed of a 25nm GaN buffer layer, a 2 μm Si doped n-type GaN layer, a five period of In_xGa_{1-x}N/GaN multiple quantum well (MQW) structure in which each period is 17nm, and a 160nm Mg doped p-type GaN layer. The average composition x of indium is around 0.2 with the quantum well thickness 3nm. The nanorods are defined on the planar LED structure by spin coating a monolayer of silica nano-particles as the etch mask. The detail of nanorod LED fabrication was reported in our previous work[1]. Figure 1. is The SEM (scanning electron microscopic) image which shows the nanorod structures with 100nm in diameter and about 300nm in depth. In order to prevent the P-N short circuit while the fabrication of metal contact, 100nm SiO₂ is deposited on the nanorod surface by plasma enhanced chemical vapor deposition (PECVD) as the passivation layer.

Next, we use mechanical polishing process to remove SiO₂ on the top of nanorods. This is similar to the chemical mechanical polishing (CMP) process which is known well in the IC fabrication industry but here we only choose the mechanical part of polishing effect. Figure 2. is the macro sketch of this method, we attach our sample on a copper column then put it on a rotational platen. As we know the surface microhardness of GaN is between 1200-1700 kg mm⁻² which is more harder than 790 kg mm⁻² of SiO₂ [4,5], so we can remove SiO₂ layer without damaging GaN by controlling the force properly on the copper column. We use Al₂O₃ particles slurry to perform the mechanical polishing process which is shown in figure 2. The particles size of Al₂O₃ is about 800nm. Figure 3. shows the micro sketch of this process. After the top area of SiO₂ layer is removed, we put on thin metal Au/Ni (5nm/5nm) as current spreading layer and Au/Ti (120nm/10nm) on both p-mesa and n- mesa as metal pad. Figure 4. shows the sketch of our nanorod LED.

RESULTS & DISCUSSION

As shown in Figure. 5., the device show a well-rectified diode behavior with a current ratio 3983 at +5V and the leakage current at -5V is about 4.77nA..

The dash line in Figure 5. shows 7.35 ideality factor of nanorod LED which is based on the diode equation

$I = I_0[\exp(Ve/nkT) - 1]$, n is the ideality factor [6].

The ideality factor of GaN based diode with multiple quantum wells is usually in the range between 5~7[7,8], which is higher than the n of common diode($n=1\sim 2$). Furthermore, the fabrication of nanorod structure diode will damage the surface of devices easily which also contribute to high ideality factor (10 and 18 [9,10]). For the relative low ideality factor of our MQW nanorod structured LED, we suggest that the SiO₂ layer passivated the surface damage through the whole fabrication process.

The luminescence-current (L-I) behavior of the nanorod LED array was measured at room temperature using an integrating sphere. Figure 6. is the luminescence-current density curve. It is shown that at 32A/cm², the luminous intensity is 6807mW/cm². The out put power of our device is the highest among similar devices reported so far. The inset of Figure 6. shows the uniform light emission surface picture of nanorod LED at 1mA which proves the great uniformity of the controlled mechanical polishing process over the whole device.

Next we want to discuss the external quantum efficiency (EQE), which is defined as the ratio between the photons extracted to free space and carriers injected to the device (assuming the same area size as the p-mesa). Usually, a so-called efficiency droop phenomena will be observed as the injection current increase. Such an interesting phenomenon leads us to study the droop effect in the nanorod LED array. Typically, for planar GaN based LED structure, the root causes of efficiency droop may be attributed to various device phenomena such as the Auger recombination, junction heating, and polarization induced carrier overflow [11-14].

In order to simplify the case, the EQE of nanorod device was extracted at both DC (100% duty cycle) and pulsed (1% duty cycle with the cycle period of 50ms) currents. The normalized EQE vs. injection current for both bias conditions are plotted in Figure 7.. Basically, the heating effect can be mitigated at the 1% duty cycle. The EQE curve at 1% duty cycle shows a nearly constant decreasing trend (with the slope $-7.69 \times 10^{-3} (A/cm^2)^{-1}$), which is close to that of the 100% duty cycle case at the high current level (slope = $-6.45 \times 10^{-3} (A/cm^2)^{-1}$). As we compared both cases, the rapid EQE decrease between 1.6 and 8A/cm² (slope = $-8.3 \times 10^{-2} (A/cm^2)^{-1}$) in Figure. 7 is mainly associated with the thermal effect, while the decrease beyond 8A/cm² is related to other factors.

CONCLUSIONS

Nanorod LED array devices were fabricated using the technology of nanosphere lithography. By spin-coating a monolayer of silica nanospheres on top of the GaN based LED epi-structure, and followed by semiconductor etching, the MQWs nanorod structure was realized. The nanorod sidewalls were passivated by PECVD grown SiO₂ and the p-type GaN tips were exposed by chemical mechanical

polishing. We achieve a reverse leakage current of 4.77nA at -5V, an ideality factor of 7.35, and an optical output intensity of 6807mW/cm² at the injection current density of 32A/cm² with very high nanorod light emitting uniformity. Moreover, the study of droop effect for such a nanorod LED array reveals that junction heating is responsible for the sharp decrease at the low current between 1.6A/cm² and 8 A/cm².

FIGURES

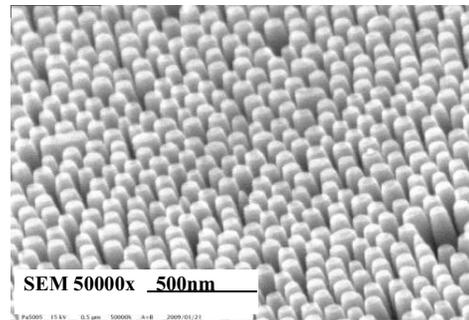


Figure 1. The SEM of nanorod structure with 100nm in diameter and 350nm in depth.

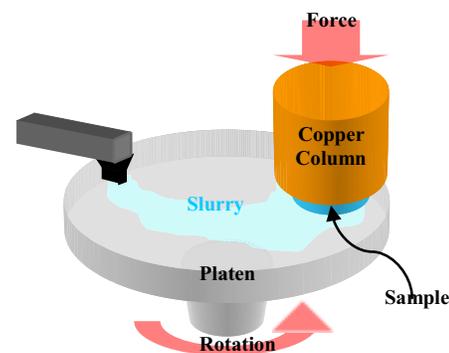


Figure 2. The macro schematic diagram of mechanical polishing process.

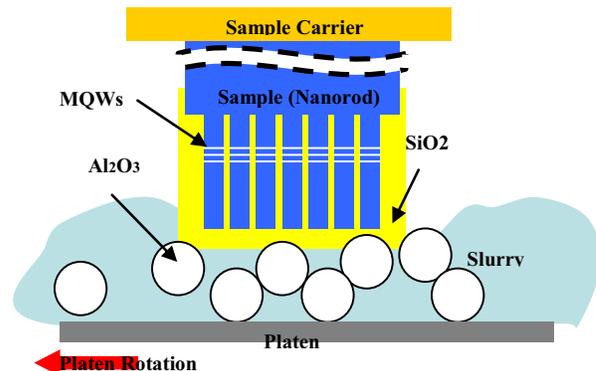


Figure 3. shows the micro schematic diagram of mechanical polishing process.

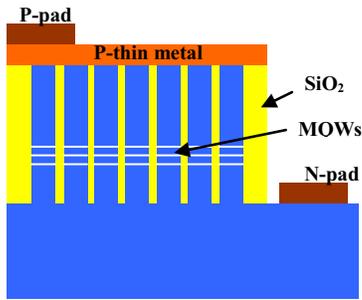


Figure 4. shows the schematic diagram of our nanorod LED.

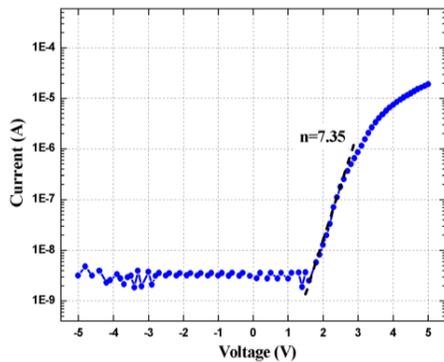


Figure 5. The diode curve of nanorod LED plotted in semi-log scale. N is the ideality factor.

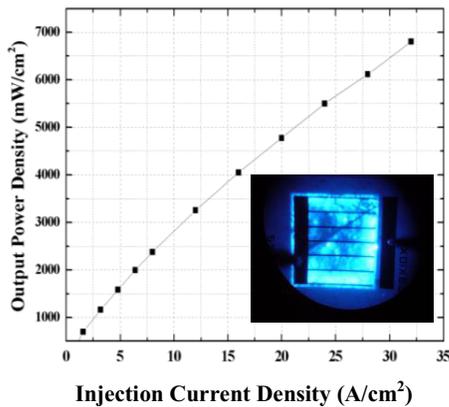


Figure 6. The L-I curve of nanorod LED (Inset is the light emission picture at 1mA injection current).

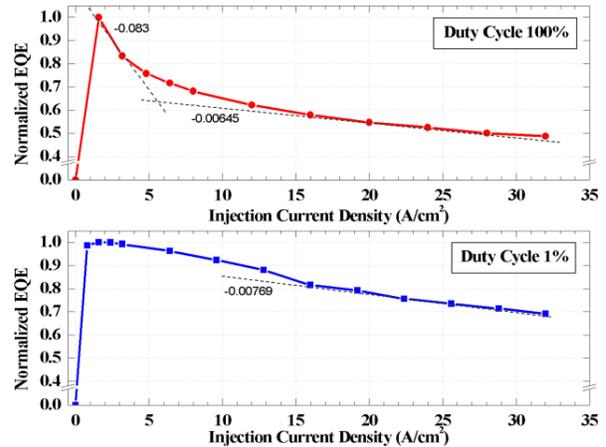


Figure 7. Normalized EQE at (a)100% and (b)1% duty cycles.

REFERENCES

- [1] C. Y. Wang, L. Y. Chen, C.P. Chen, Y. W. Cheng, M. Y. Ke and JianJang Huang, "GaN nanorod light emitting diode arrays with a nearly constant electroluminescent peak wavelength" OPTICS EXPRESS, No.14, 10556 (2008).
- [2] C. H. Chiu¹, T. C. Lu, H. W. Huang, C F Lai¹, C. C. Kao, J. T. Chu, C C Yu, H. C. Kuo, S. C. Wang, C. F. Lin and T. H. Hsueh, "Fabrication of InGaN/GaN nanorod light-emitting diodes with self-assembled Ni metal islands" Nanotechnology, vol. 18 445201 (2007)
- [3] Y. J. Lee, S. Y. Lin, C. H. Chiu, T. C. Lu, H. C. Kuo, S. C. Wang, S. Chhajed, J. K. Kim, and E. Fred Schubert, "High output power density from GaN-based two-dimensional nanorod light-emitting diode arrays, Publisher", Appl. Phys. Lett., 94, 141111 (2009). R. T. Tung, Phys. Rev. B 45, 13509 (1992).
- [4] Drory, M.D., J.W. Ager, T. Suski, I. Grzegory and S. Porowski, "Hardness and fracture toughness of bulk single crystal gallium nitride", Appl. Phys. Lett., 69, 4044-4046 (1996).
- [5] W. Alexander and J. Shackelford, CRC Materials Science and Engineering Handbook, CRC press, USA, p.474 .
- [6] S.M. Sze, Physics of Semiconductor Devices, second ed., Wiley, New York, 1981.
- [7] K. Mayes, A. Yasan, R. McClintock, D. Shiell, S. R. Darvish, P. Kung, and M. Razeghi, "High-power 280 nm AlGaIn light-emitting diodes based on an asymmetric single-quantum well," Appl. Phys. Lett. 84, 1046(2004) .
- [8] J. M. Shah, Y.-L. Li, Th. Gessmann, and E. F. Schubert, "Experimental analysis and theoretical model for anomalously high ideality factors ($n \gg 2.0$) in AlGaIn/GaN p-n junction diodes," J. Appl. Phys. 94, 2627 (2003).
- [9] P. Deb, H. Kim, Y. Qin, R. Lahiji, M. Oliver, R. Reifemberger, and T. Sands, "GaN Nanorod Schottky and p-n Junction Diodes," Nano Lett. 6, 2893-2898 (2006).
- [10] A. Motayed, A. V. Davydov, M. D. Vaudin, I. Levin, J. Melngailis and S. N. Mohammad, "Fabrication of GaN-based nanoscale device structures utilizing focused ion beam induced Pt deposition," J. Appl. Phys. 100, 024306 (2006).
- [11] M. H. Kim, M. F. Schubert, Q. Dai, J. K. Kim and E. F. Schubert "Origin of efficiency droop in GaN-based light-emitting diodes," Appl. Phys. Lett. 91, 183507 (2007)
- [12] Y. C. Shen, G. O. Mueller, S. Watanabe, N. F. Gardner, A. Munkholm, and M. R. Krames, "Auger recombination in InGaIn measured by photoluminescence," Appl. Phys. Lett. 91, 141101 (2007)
- [13] A. A. Efremov, N. I. Bochkareva, R. I. Gorbunov, D. A. Lavrinovich, Yu. T. Rebane, D. V. Tarkhin and Yu. G. Shreter, "Effect of the joule

heating on the quantum efficiency and choice of thermal conditions for high-power blue InGaN/GaN LEDs,” *Semiconductors*, 40, 605–610 (2006)

- [14]H. S. Chen, D. M. Yeh, Y. C. Lu, C. Y. Chen, C. F. Huang, T. Y. Tang, C. C. Yang, C. S. Wu and C. D. Chen, “Strain relaxation and quantum confinement in InGaN/GaN nanoposts,” *Nanotechnology*, 17, 1454–1458 (2006).