

## MEETING THE FABRICATION CHALLENGES FOR BACKSIDE PROCESSING ON THIN SUBSTRATES WITH ULTRAHIGH DEVICE TOPOGRAPHY

Pavan Bhatia<sup>1</sup>, Roberta Hawkins<sup>1</sup>, Jan Campbell<sup>1</sup>, Martin Ivie<sup>1</sup>, Alex Smith<sup>2</sup>, Mark Privett<sup>2</sup>, Gary Brand<sup>2</sup>

<sup>1</sup>) TriQuint Semiconductor, 500 W. Renner Road, Richardson, TX 75080

<sup>2</sup>) Brewer Science, Inc., Rolla, MO  
Paven.Bhatia@tqs.com 972-333-8793

### Abstract:

**TriQuint manufactures devices on ultrathin substrates (100  $\mu\text{m}$ ) with ultrahigh topography ( $>80 \mu\text{m}$ ). The development of a mounting process that enables thinning of the substrate to the required thickness for such devices is presented with consideration to critical process requirements including total thickness variation (TTV), grind thickness uniformity, damage removal, etch resistance, and electrical parameters.**

### INTRODUCTION

As wafer thickness decreases to 100  $\mu\text{m}$  and thinner, manufacturing challenges arise. Ultrathin wafers in the 100- $\mu\text{m}$  thickness range with ultrahigh topography ( $>80 \mu\text{m}$ ) are less stable and more vulnerable to stresses, and the die can be prone to breaking and warping—not only during grinding but also at subsequent processing steps.

One of the major requirements of mounting ultrahigh device wafers to carriers is to protect the active device during the thinning process. Protection must be provided by means of a consistent process that meets tight total thickness variation (TTV) requirements across the stack. Previously, large volumes of mounting material have been required to protect these ultrahigh devices. Mounting materials can be quite expensive; therefore, reducing the amount of material used per wafer is key to minimizing cost. Methods for developing a process that uses less material while still meeting all of the basic requirements are presented.

### BACKGROUND

TriQuint manufactures semiconductor devices that require thinning of the 6-inch device

wafer to as thin as 100  $\mu\text{m}$  while supporting over 80  $\mu\text{m}$  of topography. TriQuint currently uses a material and process that require multiple coating and baking steps to sufficiently cover device topography. In an effort to reduce cost and simplify the mounting process, TriQuint has started collaboration with Brewer Science, Inc. A coating and bonding process has been developed which allows achievement of 100- $\mu\text{m}$  post-grind substrate thickness and sufficiently covers TriQuint topography in a single coating step using a Brewer Science bonding material (WaferBOND<sup>®</sup> HT-10.10 bonding material).

Tests for feasibility were first conducted at Brewer Science using its equipment. These tests were then followed up with several additional sets of tests at TriQuint using Brewer Science bonding material and demounting solution while using TriQuint's processing equipment.

### METHOD

For the initial demonstration, 10 product wafers and carriers were bonded and mounted at Brewer Science, with an additional 10 device wafers coated (but not bonded) at Brewer Science. The bonded pairs were returned to TriQuint for device wafer thinning to 120  $\mu\text{m}$ . The unbonded wafers were bonded and thinned at TriQuint.

All bonded pairs were initially scanned for voids using the Sonix<sup>®</sup> Fusion<sup>®</sup> scanning acoustic microscope (SAM). The carrier identification number was used to track each pair in this state. Figure 1 displays one of the scans.

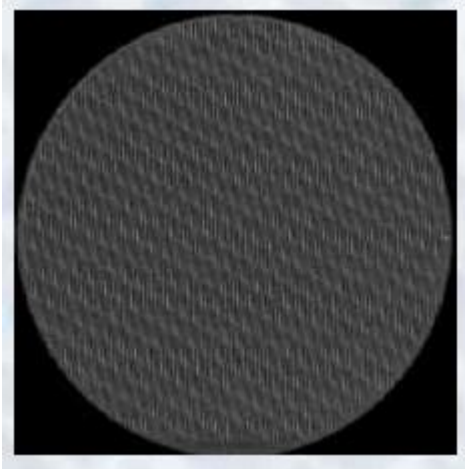


Figure 1: SAM Map

## RESULTS

Each of the bonded pairs showed uniform bonding lines regardless of whether they were bonded at TriQuint or Brewer Science. Thickness and variation across the wafer were also measured. TriQuint has created an algorithm to measure thickness uniformity in terms of wedge, bump, dip, and photo bump (bump in the center of the wafer), as shown in Figure 2. Green represents “good” while yellow represents “marginal” and red represents “rework.” The image shows that all wafers measured from the first demonstration passed without requiring rework.

Wafer	Wedge	Bump	Dip	Photo bump
W02	Green	Yellow	Yellow	Yellow
W03	Green	Green	Green	Yellow
W04	Green	Green	Green	Green
W05	Green	Green	Green	Green
W06	Green	Green	Green	Yellow
W07	Green	Green	Green	Green
W08	Yellow	Green	Green	Yellow
W09	Green	Green	Green	Green
W10	Green	Green	Yellow	Green
W11	Green	Green	Green	Green

Figure 2: Post-Mount Thickness Uniformity

In addition to mounting, many challenges associated with demounting and cleaning the

wafers had to be overcome. Each step in the mounting process proved to play a significant role in the demounting and clean processes, making it even more critical that the mounting process was properly optimized.

## CONCLUSIONS

The Brewer Science® WaferBOND® HT-10.10 temporary bonding material showed excellent results while decreasing the amount of the material used compared to the standard bonding material.

## ACKNOWLEDGEMENTS

## ACRONYMS

SAM: Scanning Acoustic Microscope  
TTV: Total Thickness Variation