

MOCVD Growth of AlGaN/GaN Heterostructures on 150 mm Silicon

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Abstract

Growth of crack-free AlGaN/GaN heterostructures on 150mm Si by metal-organic chemical vapor deposition is presented. The two-dimensional-electron-gas is formed at the AlGaN/GaN interface. Average Hall mobility values are more than 2000 cm²/V.s and sheet resistance less than 400 Ohm/sq with a 1nm AlN spacer. Run-to-run repeatability of AlGaN/GaN structural qualities, wafer bow, and 2DEG properties shows the potential manufacturing possibility.

INTRODUCTION

Due to the low cost and availability of Si substrates with larger diameters (> 150 mm) compared with sapphire and SiC substrates, AlGaN/GaN based High Electron Mobility Transistors (HEMTs) on Si have emerged as a promising candidate for high frequency power amplification and high voltage power switching applications.

With the large mismatch of lattice constant and thermal expansion coefficients between Si and GaN, epitaxy on Si leads to problems such as cracks and high density misfit and threading dislocations. Many techniques have been utilized to relieve the stress and create crack-free GaN on Si, such as low-temperature AlN layer [1], graded AlGaN buffer layers [2-3], AlN/GaN superlattices [4], SiC intermediate layer [5], and SiN interlayer [6]. In addition, large wafer bow caused by the compressive stress from GaN during growth hinders the uniform temperature control across the wafer, which results in the non-uniform composition/thickness, layer stress and device performance. With the growth challenges of heteroepitaxy of GaN on Si, device quality GaN and manufacturability have to be demonstrated for the potential mass production and broad applications of AlGaN HEMTs on Si.

EXPERIMENTAL RESULTS AND DISCUSSION

Here we report the growth of crack-free AlGaN/GaN heterostructures on 150 mm (111) oriented silicon substrates by metal-organic chemical vapor deposition (MOCVD). The epitaxy process is carried out in a 5x150mm multi-wafer Veeco Turbodisc K465i production MOCVD system equipped with a DRT-210 *in-situ* process monitor

(integrated pyrometer-reflectometer-deflectometer unit) for wafer temperature, reflectance and wafer curvature.

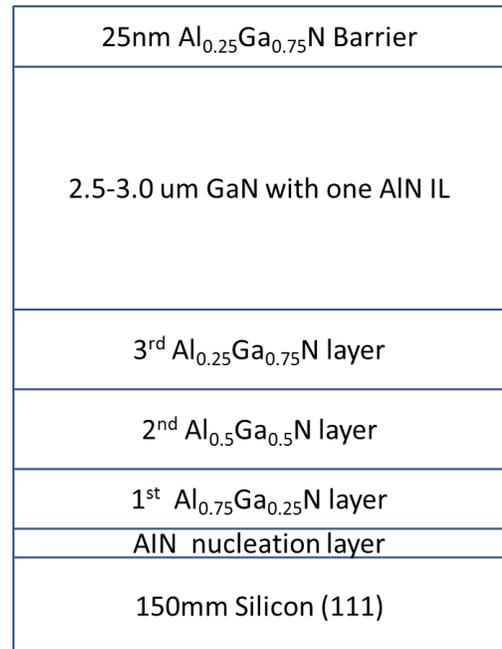
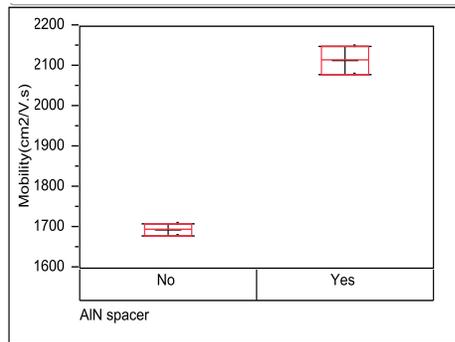


Fig. 1: Schematic of AlGaN/GaN heterostructures on 150 mm (111) Si

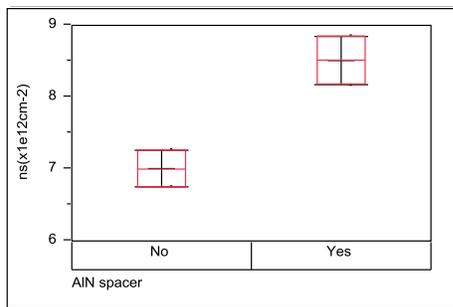
The schematic of the AlGaN/GaN heterostructure is shown in Fig. 1. The growth does not require any pre-growth wafer conditioning. The starting Si wafer is first annealed at 1050 °C under hydrogen ambient for about 5 min, which is followed by the pre-flow of TMAI without the presence of ammonia. The buffer layers consist of a 100-200 nm AlN nucleation layer and three step-graded Al_xGa_{1-x}N intermediate layers with x=0.75, 0.5, and 0.25. The total thickness of the three Al_xGa_{1-x}N layers is ~800 nm. The growth conditions for AlN and AlGaN buffer layer are above 1000 °C at a pressure of 75-100Torr. About 2-3 μm GaN layer is grown on top of the AlGaN buffer layers with one optional stain-relieving AlN interlayer. The unintentionally doped Al_{0.25}Ga_{0.75}N barrier layer is grown without a GaN cap in order to inspect the AlGaN surface

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a

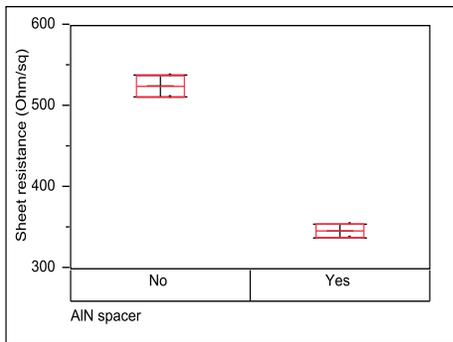
morphology. An optional 1-nm thick AlN spacer can be inserted between the AlGa_N/Ga_N interfaces to improve the performance of the two-dimensional electron gas (2DEG).



(a)



(b)

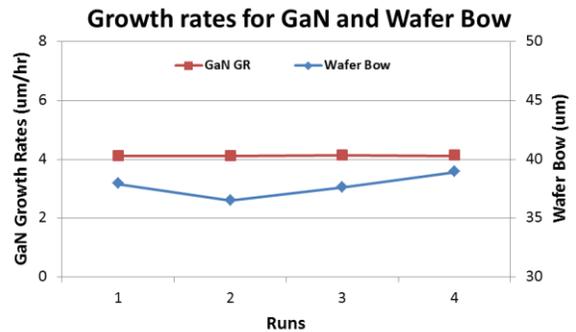


(c)

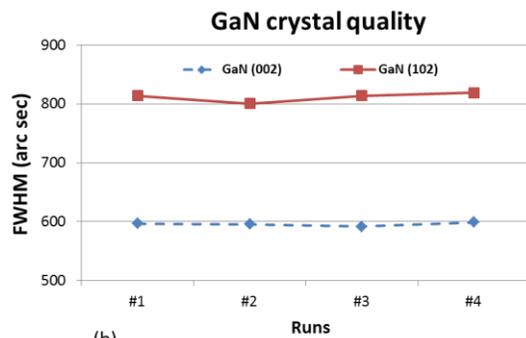
Fig. 2: Electrical properties of the two-dimensional-electron-gas of AlGa_N/Ga_N heterostructures with and without 1nm AlN spacer. (a) Mobility, (b) Sheet carrier concentration, (c) Sheet resistance

The crystalline quality of Ga_N layer is measured using high resolution X-Ray Diffraction (HRXRD) for (002) symmetrical and (102) asymmetrical Bragg reflections. The thickness of epilayers is measured by white light interference and Al composition by photoluminescence. The surface morphology of the AlGa_N is studied by Atomic Force Microscope (AFM), and the electrical properties of the 2DEG are evaluated by Van der Pauw-Hall measurement using indium dots as the Ohmic contact.

As grown AlGa_N/Ga_N heterostructures are uniform and free of cracks from the edge to the wafer center. Typically uniformity of <1.5%, 1σ is measured for thickness, and uniformity of <1.5%, 1σ for Al composition. For a 25nm Al_{0.25}Ga_{0.75}N barrier, the AlGa_N/Ga_N heterostructure exhibits an average 2DEG mobility (μ_H) > 1500 cm²/V.s, sheet carrier concentration (n_s) > 6.0 × 10¹² cm⁻², and an average sheet resistance (R_{sh}) < 600 Ohm/sq. With the insertion of a 1nm AlN spacer at the AlGa_N/Ga_N interface, the average mobility μ_H is > 2000 cm²/V.s, n_s > 8.0 × 10¹² cm⁻², and R_{sh} < 400 Ohm/sq, as shown in Fig. 2 (a), (b) and (c). The enhancement of 2DEG performance compared to those without AlN spacer could be due to the produced large effective conduction band offset between AlGa_N and Ga_N (ΔE_c), reduction of the alloy disorder scattering, and improved polarization. [7]



(a)



(b)

Fig. 3: Run-to-run repeatability for AlGa_N/Ga_N heterostructures without AlN spacer. (a) Growth rates of GaN and wafer bow, (b) GaN crystal quality of FWHM (002) & FWHM (102)

Fig. 3 shows the Run-to-run (RtR) repeatability for the AlGa_N/Ga_N heterostructures without AlN spacer. The growth rates of GaN show variation of < 0.3%, 1σ from the 4 repeat runs. The GaN crystalline quality is analyzed by HRXRD. The full width at half maximum (FWHM) for rocking curves of (002) and (102) diffraction planes of Ga_N show an average of 595 and 812 arc sec, with variation of

<1.2%, 1σ . The wafer bows are consistently <50 μ m measured at room temperature for the \sim 3 μ m stack. The *in-situ* measurement shows repeatable reflectivity and wafer curvature for all the runs. The reflectivity of the repeat runs is shown in Fig. 4.

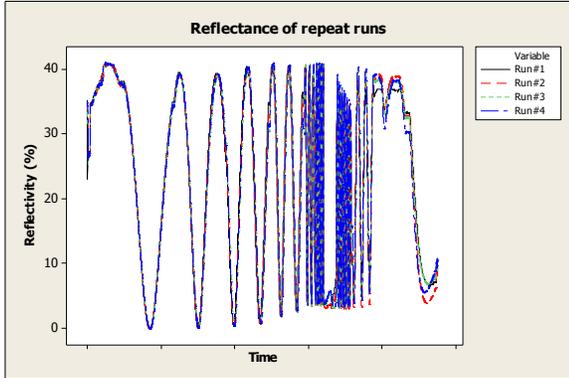


Fig. 4: Run-to-run repeatability of *in-situ* reflectance measured by DRT for AlGaIn/GaN heterostructures

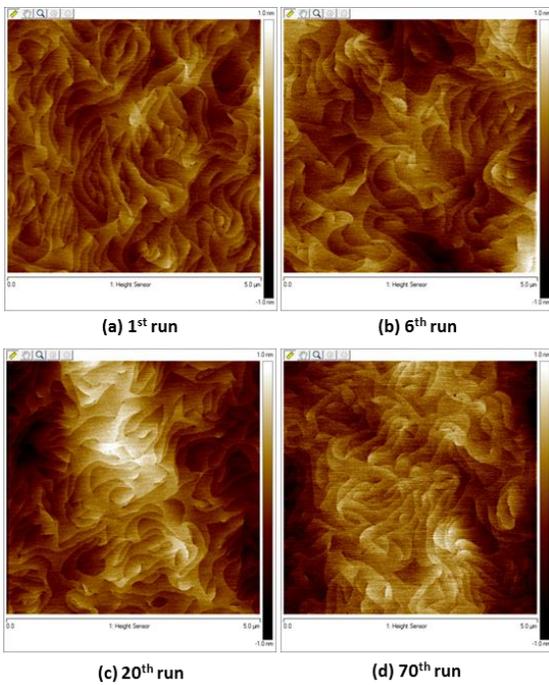


Fig. 5: 5 μ m \times 5 μ m AFM images of repeat runs of AlGaIn heterostructures

The surface morphology of the AlGaIn barrier without AlN spacer is shown in Fig. 5 (a)-(d) for the repeat runs (within 70 continuous runs without chamber opening). The AlGaIn surface shows no micro-cracks and a step-flow feature comparable to the surface of the underlying GaN layer, indicating the pseudomorphic growth of AlGaIn with less

strain relaxation. The root-mean-square (RMS) roughness is around 0.2-0.4 nm in a 5 μ m \times 5 μ m scanned area.

Hall measurement is done on three points on the wafer from the center to the edge. The typical values of mobility, sheet carrier concentration, and sheet resistance across the wafer show variation of <10%, 1σ . Hall measurement for these AlGaIn/GaN heterostructures without AlN spacer show comparable mobility, sheet carrier concentration, and sheet resistance, as shown in Fig. 6 (a), (b), and (c).

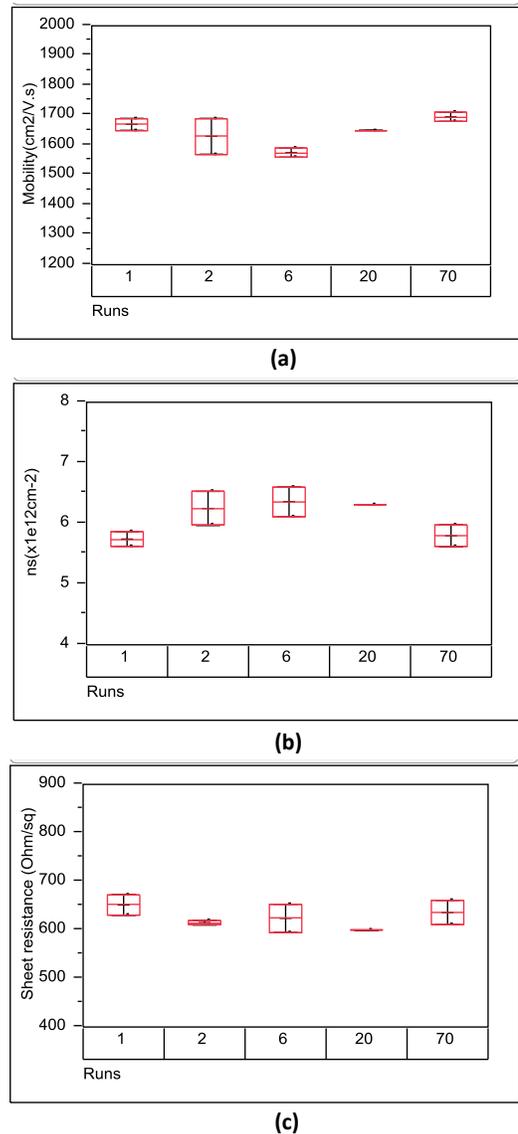


Fig. 6: 2DEG performance of repeat runs of AlGaIn heterostructures without AlN spacer. (a) Mobility, (b) Sheet carrier concentration, (c) Sheet resistance

CONCLUSIONS

In summary, we report here the MOCVD growth of crack-free AlGaIn/GaN heterostructures on 150 mm silicon by MOCVD. The AlGaIn/GaN heterostructures show high 2DEG mobility $> 2000 \text{ cm}^2/\text{V}\cdot\text{s}$, low sheet resistance $< 400 \text{ Ohm}/\text{sq}$ with a 1nm AlN spacer. Good run-to-run repeatability of AlGaIn/GaN structural qualities, wafer bow, and 2DEG properties shows the possibility of using Si as platform for the manufacturing of AlGaIn/GaN based HEMT devices.

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ACRONYMS

MOCVD: Metal-Organic Chemical Vapor Deposition

HEMT: High Electron Mobility Transistors

2DEG: Two-Dimensional Electron Gas

AFM: Atomic Force Microscope

HRXRD: High Resolution X-Ray Diffraction

PL: Photoluminescence

FWHM: Full Width at Half Maximum

RMS: Root-Mean-Square

RtR: Run-To-Run